



Vision AI accelerator IP

Effortlessly deploy sub-mW vision tasks at the very edge

Expend the least energy to get actionable insights quickly without compromising on performance



Optimized ML models

Push button ML deployment tool

Scalable hardware accelerator IP

IPS/W

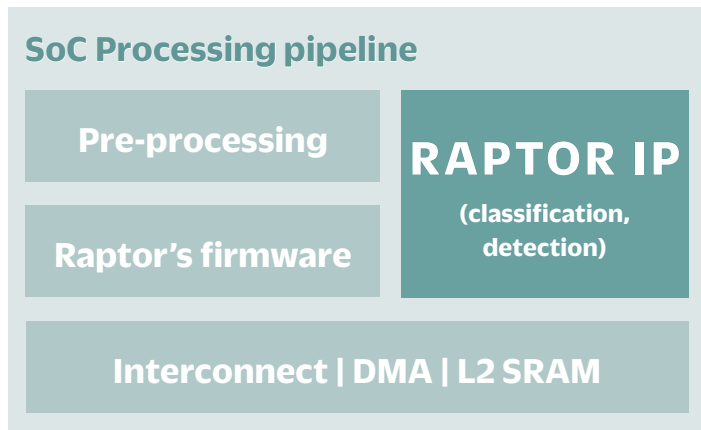
X10

Alternatives



AI Vision made viable for constrained devices

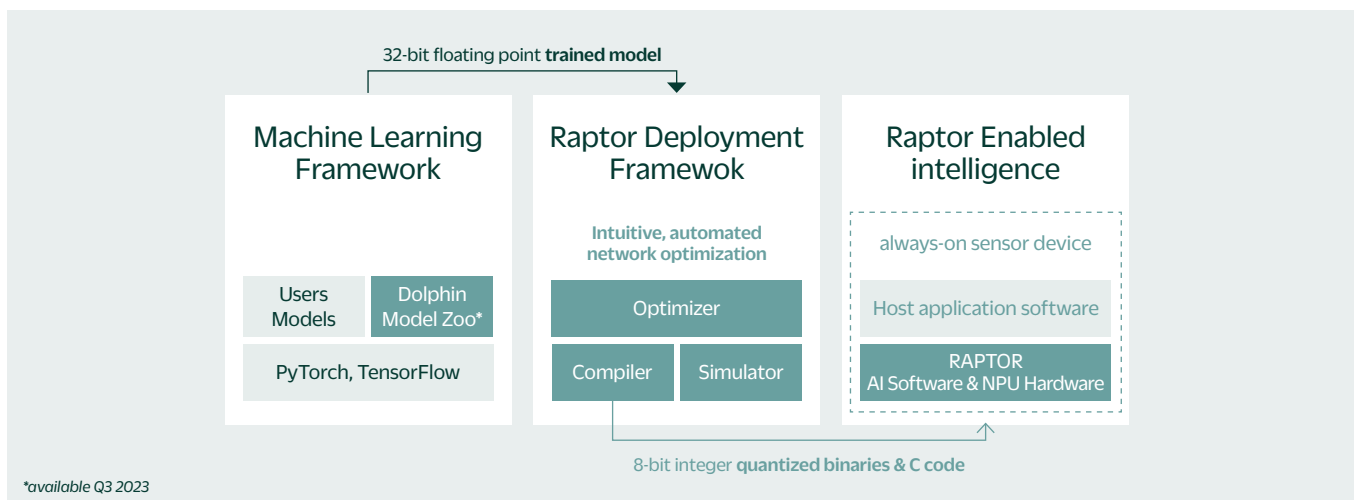
Application example: 4 hands gesture recognition – SoC processing pipeline power & performances



- ▶ Raptor's host FW: 32 kB
- ▶ ML model(s) size: 130 kB
- ▶ ML perf.: mAP@0.5: 72%
- ▶ Power: 0.7 mW

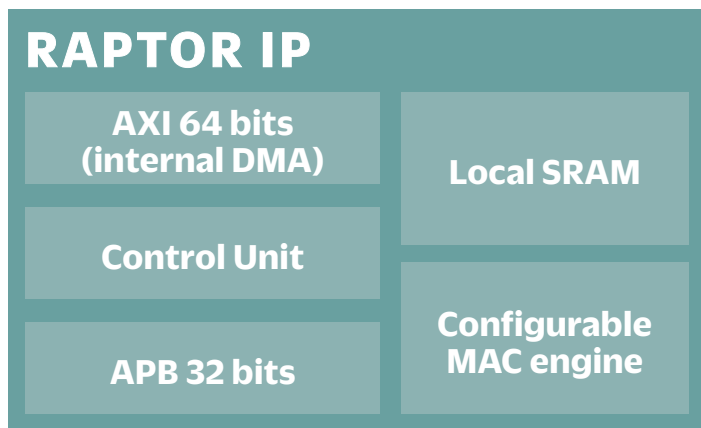
From ML models exploration to deployment, in few clicks

Simulate speed & power for different configurations, build & deploy your application code in no time



*available Q3 2023

Scalable range of performances, portable to any process



- ▶ MAC/cy (8*8): 32, 64, 128
- ▶ Local SRAM (kB): 128, 256, 512
- ▶ Data types: 8bits INT
- ▶ Efficient activation mapping: tiling & data reuse
- ▶ ML frameworks: Keras/tensorFlow, Pytorch, ONNX

